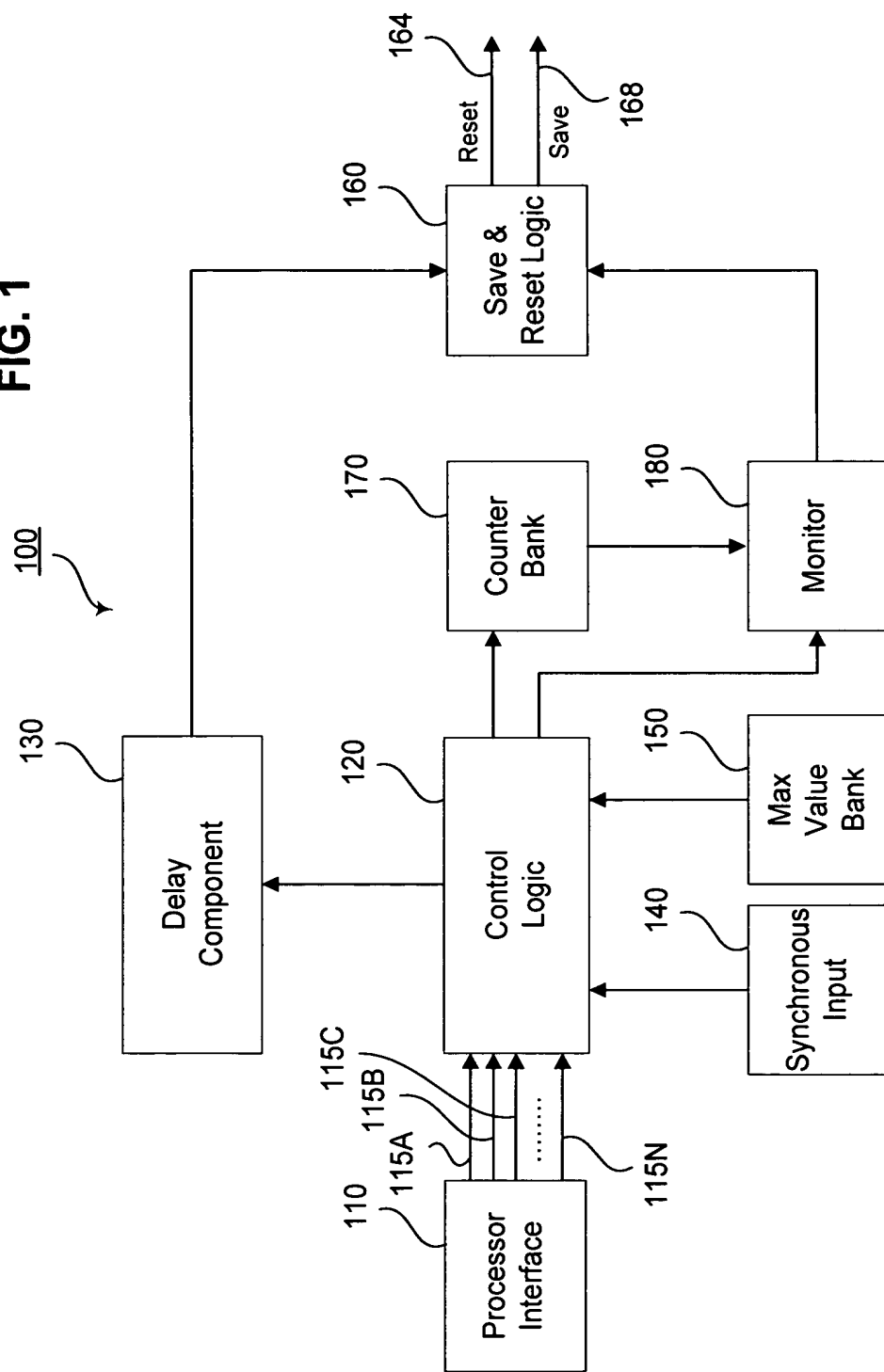


FIG. 1



200

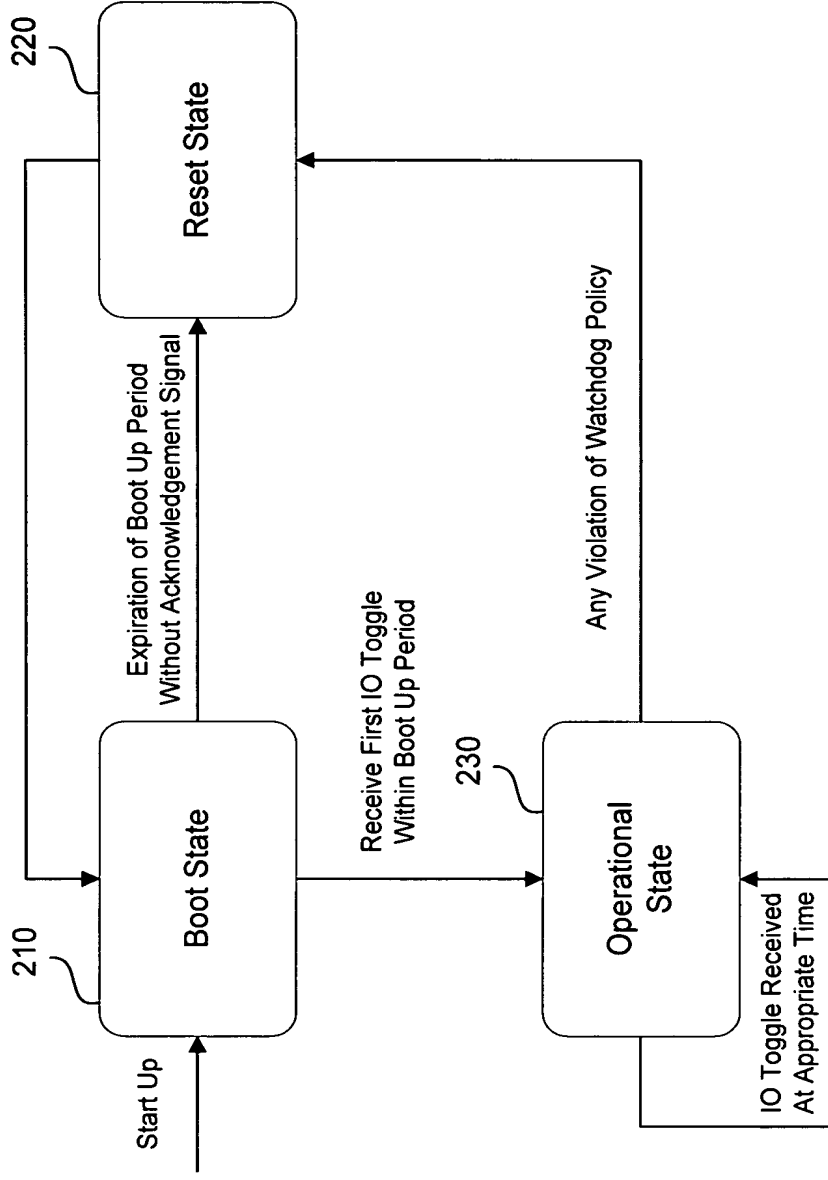
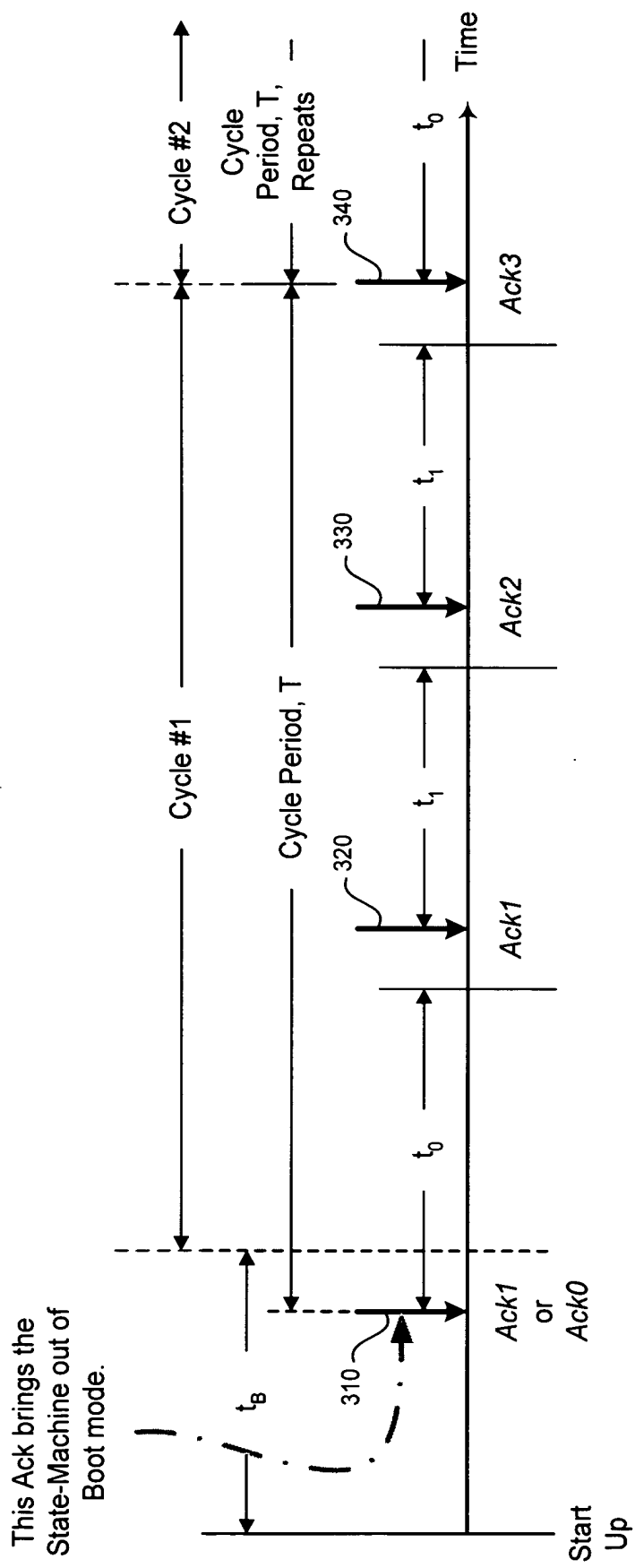


FIG. 2



Here $N = 3$, and t_B is Boot timer, t_0 is Forbidden Timer, and t_1 is Acknowledgement Timer.

The Sequence of Acks ($Ack1$, $Ack2$, and then $Ack3$) has to be same on every Cycle Period T .

FIG. 3